Modeling Circuit Systems Coupled with Distributed Semiconductor Equations

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Abstract

For a refined network analysis, we are interested in circuit simulation including distributed models of semiconductors. We construct a mathematical model for nonlinear electric networks containing semiconductors described by the drift-diffusion equations. The focus lies on the coupling of the network DAEs and the semiconductor PDEs.

Furthermore, we study the behavior of the coupled systems with respect to time dependent perturbations using an index concept for abstract DAEs. We present a network topological criterion that guarantees index-1 systems.

1 Introduction

The miniaturization trend in the development of integrated circuits has reached a level where certain physical effects as, for example, parasitic capacities, temperature dependency and quantum effects cannot any longer be neglected. This implies a refined modeling of the devices which yields to comprehensive replacement circuits, usually called as compact models, for the circuit simulation.

The development of these compact models and the correct tuning of the big number of model parameters is nowadays so expensive that a direct coupling of circuit and device simulation becomes desirable. Naturally, a coupling of both simulations (see e.g. [17, 26, 6, 4]) is very time consuming. However, in case of large circuits with a few critical semiconductor devices (used e.g. in RF applications), we expect that the simulation costs of such a coupled system become sufficiently low with regard to the short development cycles.

Aiming a development of fast and reliable simulations of the coupled systems, we start with constructing and analyzing the model equations. Regarding that the sensitivity of numerical solutions of DAEs with respect to perturbations depends strongly on the DAE index, we are interested in an adequate criterion for the coupled system. Therefore, we investigate the coupled system as abstract differential algebraic system (ADAS) and its index (see [13]). The ADAS index concept orientates on the tractability index for DAEs. It characterizes the sensitivity of the solution with respect to time dependent perturbations and it represents an extension of the time index introduced in [15] for time-dependent and nonlinear PDAEs. We neglect perturbations with respect to space here

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because, in circuit simulation, the main interest is devoted to the simulation of the transient behavior of the circuit.

Considering the results concerning the coupling of different circuit models [3], we can not expect that the index of the coupled system does not exceed the index of the sub-systems. Therefore, we focused our investigations to the question under which conditions the coupled system has index one, which means that the system is well-posed considering perturbations with respect to time. In [2], it has been shown that the index is one for network models including the stationary drift-diffusion equations if the network itself has index 1 and all semiconductors are connected by a capacitive path. Here, we consider network models including the instationary drift-diffusion equations and we are interested in necessary as well as sufficient criteria for index-1 systems.

The present paper is organized as follows. Section 2 introduces the network models described by a differential algebraic system. In Section 3, we describe the drift diffusion model for semiconductor elements. Section 4 is devoted to the modeling of the coupling of both systems. Finally, in Section 5, we consider the coupled system as abstract differential algebraic system and investigate the index as a measure for the behavior of the system with respect to time dependent perturbations.

2 Network Modeling

The numerical simulation of electric networks is closely connected to the network modeling. Circuit models have to meet two contradicting demands. On the one hand, they have to describe the physical behavior of the circuit as correct as possible. On the other hand, the models should be as simple as possible in order to reduce the computing time to a minimum.

A well established approach meeting both demands to a certain extent is the description of the network by the Modified Nodal Analysis (MNA). It bases on Kirchhoff's laws and the voltage-current relations of the network elements.

Introducing j as the vector of all branch currents, v as the vector of all branch voltages and e as the vector of all node potentials (voltage with respect to the mass node), we may express Kirchhoff's current law (KCL) as

$$Ai = 0, (1)$$

and Kirchhoff's voltage law (KCL) as

$$v = A^{\mathrm{T}}e.$$
 (2)

The matrix A represents the incidence matrix describing the node-to-branch relations. It is constant and has the entries 1, -1 and 0 only. The voltagecurrent relation of basic network elements like resistances, capacitances and inductances may be described by

$$I_R = g(v_R), \quad i_C = \frac{\mathrm{d}q_C}{\mathrm{d}t}(v_C), \quad v_L = \frac{\mathrm{d}\phi_L}{\mathrm{d}t}(i_L)$$

respectively. For the description of more complex devices like transistors, the use of so-called compact models has been proven to be very efficient. The compact models represent replacement circuits of the device using basic network elements only. In Figure 1 you see a simple compact model for a metal-oxide-



Figure 1: Compact model for a MOSFET

semiconductor field effect transistor (MOSFET).

Finally, the MNA leads in its charge oriented form to an equation system of the form

$$A_C \frac{\mathrm{d}q(A_C^{\mathrm{T}}e)}{\mathrm{d}t} + A_R g(A_R^{\mathrm{T}}e) + A_L j_L + A_V j_V = -A_I i_s, \qquad (3)$$

$$\frac{\mathrm{d}\phi(j_L)}{\mathrm{d}t} - A_L^{\mathrm{T}}e = 0, \qquad (4)$$

$$A_V^{\mathrm{T}}e = v_s. \tag{5}$$

Here, q and ϕ describe the charges and fluxes, respectively. A_C describes the incidences of all capacitive branches. Correspondingly, R, L, V and I stand for resistances, inductances, voltage sources and current sources. The input functions i_S and v_S describe the current and voltage sources, respectively. For brevity, we consider only independent sources. This implies that i_S and v_S depend on time only. For a more detailed discussion of the MNA, we refer to [5].

The further miniaturization of the network elements leads to more and more complex compact models, which may contain more than 500 parameters today. The development of the compact models and the extraction of their parameters becomes so expensive that it is worth to consider the inclusion of original physical device models directly in the network simulation. The next section is devoted to the description of such models.

3 Semiconductor Device Modeling

Considering the literature, one finds an enormous amount of books and papers dealing with semiconductor device modeling. We want to mention here [20, 7,

8, 16, 24]. These contributions provide a comprehensive overview of the topic and focus onto the mathematical background.

Semiconductor device models describe the electron transport in the bulk of the semiconductor. In consideration of the degree of simplification one distinguishes between quantum level transport, semi-classical transport and balance equations. The first one yields the Schrödinger equation and the second one leads to the Boltzmann equation. Simplifying the Boltzmann equation further by the method of moments, we get the so called energy balance equations (considering four moments) or the drift diffusion equations (if we consider only two moments).

From the practical point of view, the interest in semiconductor device modeling is to replace as much laboratory testing as possible by numerical simulation in order to minimize the costs. This implies that the involved mathematical models cannot be too complicated. For most semiconductor technologies, the drift diffusion equations seem to represent a reasonable compromise between computational efficiency and an accurate description of the underlying physics.

However with the increased miniaturization of semiconductor devices, one comes closer and closer to the limits of validity of the drift diffusion equations. The reason for this is, on one hand, that in ever smaller devices the free carriers can not longer be modeled as a continuum. On the other hand, the drift diffusion equations are derived through a limiting process where the mean free path of a particle tends to zero. Through miniaturization this mean free path becomes larger and larger in comparison to the size of the device. In addition, quantum mechanical effects play a more an more important role in novel device structures.

But in spite of that the drift diffusion equations remain an important tool since the microscopic effects not described by them appear only locally. Thus, the most likely approach will be to use more sophisticated models only locally, and to use the drift diffusion equations in the parts of the device where they are sufficient to describe the physics (usually in the bulk of the semiconductor).

Therefore, we are concentrating here onto the drift diffusion equations considered as an important model description for the device part in coupled network and device simulation.

3.1 The Drift Diffusion Model for Semiconductor Devices

The model equations are given by the Poisson equation

$$\operatorname{div}\left(\varepsilon\operatorname{grad}V\right) = q(n-p-N) \tag{6}$$

for the electrostatic potential V and the continuity equations

$$-\partial_t n + \frac{1}{a} \operatorname{div} J_n = R(n, p, J_n, J_p, \operatorname{grad} V), \tag{7}$$

$$\partial_t p + \frac{1}{a} \operatorname{div} J_p = -R(n, p, J_n, J_p, \operatorname{grad} V)$$
(8)

for the electron and hole concentration, n and p, respectively. The parameter q denotes the elementary charge. Furthermore, ε represents the material dependent dielectric constant. J_n and J_p describe the densities of the electron

and hole current that is assumed to be a composition of a drift and a diffusion current. Consequently,

$$J_n = q(D_n \operatorname{grad} n - \mu_n n \operatorname{grad} V), \qquad (9)$$

$$J_p = q(-D_p \operatorname{grad} p - \mu_p p \operatorname{grad} V).$$
(10)

Here, qD_n grad n describes the diffusion part of the electron current caused by the effort to achieve the chemical equilibrium. The drift part $-q\mu_n n$ grad V is caused by the electric field by the effort to achieve the electrical equilibrium. The mobilities μ_n and μ_p as well as the diffusivities \mathcal{D}_n and D_p are bounded, strictly positive functions. They may depend on position x (due to dependency on doping) and on the electrical field grad V. For a more detailed discussion of the model equations see e.g. [20, 21].

The system (6)-(10) represents a system of five coupled partial differential equations. The Poisson equation (6) is of elliptic type. Regarding the current density equations (9) and (10), the continuity equations (7) and (8) are of parabolic type.

Note that we assume a constant temperature. It is justified for applications with low performance devices. In case of high performance devices, one has to consider the temperature T as a variable. The drift diffusion equations have to be completed by an energy balance equation (see e.g. [25, 12, 1]). It is a future task to combine such energy models with the network equations. In this paper, we shall concentrate on the network-device coupling using the classical instationary drift diffusion equations.

3.2 The Boundary Conditions

There are essentially three different types of materials a semiconductor is bounded by. The contacts between the network and the semiconductor are usually layers of metal. The second kind of bounding materials are insulators (e.g. oxide). Finally, they may be bounded by other semiconductors. Such semiconductorsemiconductor interfaces are called heterojunction. Here, we consider metal contacts only, since we restrict to the one-dimensional case for the first study. For a discussion of other boundary types, we refer to [19, 22].

Metal contacts are usually modeled as ohmic or Schottky contacts. Both types of contacts imply Dirichlet boundary conditions for the electrostatic potential V. Furthermore, we get Dirichlet boundary conditions for the electron and hole concentrations, n and p, in case of Ohmic contacts. For Schottky contacts, we have Neumann boundary conditions for the electron and hole current densities, J_n and J_p . For brevity and the sake of simplicity, we restrict to ohmic contacts.

Ohmic contacts are characterized by a high doping of the semiconductor. This implies a large band bending and a very thin barrier at the metal-semiconductor interface. In this case, tunneling of electrons is the dominant transport mechanism. It leads to high current densities at low voltage drops and, consequently, to a low resistance of the contact.

Since tunneling is not included in the drift-diffusion equations describing electron transport in the semiconductor volume, one should place the actual boundary for the simulation domain at the edge x_T , that means at the end of the tunneling region. At high doping concentrations, the tunneling length comprises the total depletion region, and the boundary is placed at the depletion layer edge. Consequently, we have charge neutrality at the actual boundary that means

γ

$$n - p - N = 0.$$

Furthermore,

$$V = V_{\rm ap} + V_{\rm bi},\tag{11}$$

where $V_{\rm ap}$ is the applied voltage and $V_{\rm bi}$ is the so called builtin potential of the semiconductor. The builtin potential depends on semiconductor material, on doping concentration, and on temperature. Since we consider the temperature as constant, $V_{\rm bi}$ is a given function of the position variable x only. The applied potential $V_{\rm ap}$ depends on the node potentials e at the contacts.

For very high doping (ideal ohmic contact), the resistance tends to zero which implies

$$np = n_i^2$$

with the intrinsic concentration n_i depending on material and temperature. This leads to Dirichlet boundary conditions for the electron and hole concentrations

$$n = \frac{1}{2}(\sqrt{N^2 + 4n_i^2} + N) \quad \text{on } \Gamma,$$
 (12)

$$p = \frac{1}{2}(\sqrt{N^2 + 4n_i^2} - N)$$
 on Γ . (13)

Note, that the doping density N and the intrinsic density n_i depend on the position x only.

4 Coupling of the Network and the Device Model Equations

From the engineering point of view, the coupling of network and device simulation is not a new task (see e.g. [4, 18, 26, 14]).

In contrast, the mathematical analysis of coupled network and device model equations represents a very young research field. First results have already been obtained in [9] and [8]. In [9], one semiconductor connected to a resistance has been considered. In [8] the connection of one semiconductor with a simple circuit has been studied. Simple means here that the currents entering the semiconductor may be expressed by a function of the applied voltages. In both cases, the network is treated as a special boundary condition for the semiconductor. This approach fails if more than one device belongs to the network. This is, in particular, the case for integrated circuits. However, the approach may be extended by a modification of the considered operator equation (see [22]).

More recently, networks containing uniform lossy transmission lines have been investigated in [11] and [10]. The resulting equation systems represent also coupled systems of differential algebraic equations and partial differential equations. In contrast to the case here, the PDEs are of hyperbolic type.

To the authors knowledge, an existence analysis for integrated networks containing semiconductor device models has only been developed in [2]. There, the stationary drift diffusion model for diodes have been considered. Here, we are dealing for the first time with the instationary case.

For brevity, we shall formulate the coupled system for a network that contains exactly one semiconductor. However, one can simply verify that the following arguments remain true if one considers a network with several semiconductors.

Considering the network equations (3)-(5), the task is now to include semiconductor devices that are described by the drift-diffusion model. Consequently, the currents of these semiconductor devices have to be added to the KCL equation (3). We denote the vector of all currents leaving semiconductor devices by \hat{j}_S . Let b_S be the number of all these branch currents and let n_N be the number of all nodes of the network. Then, we introduce the matrix $\hat{A}_S \in L(\mathbb{R}^{b_S}, \mathbb{R}^{n_N-1})$ with the entries

$$\hat{a}_{ik} := \begin{cases} 1 & \text{if the current } j_{S_k} \text{ enters node } i, \\ 0 & \text{else.} \end{cases}$$
(14)

This way, the matrix \hat{A}_S describes the incidence of currents of semiconductor devices. However, it differs from the other incidence matrices A_E (*E* corresponds to one of the basic network elements) by the fact that each column contains only entries 1 but no -1. We arrive at

$$A_{C}\frac{\mathrm{d}q}{\mathrm{d}t} + A_{R}g(A_{R}^{\mathrm{T}}e) + A_{L}j_{L} + A_{V}j_{V} + \hat{A}_{S}\hat{j}_{S} = -A_{I}i_{s}$$
(15)

instead of (3). Let Γ_k be the k-th contact. Then, the current flowing through terminal k is given by

$$j_{S_k} = \int_{\Gamma_k} J_{tot} \cdot \nu \, \mathrm{d}\sigma.$$

 J_{tot} represents the total current density and ν is the outward unity normal vector at a point of Γ_k . In the semiconductor we meet three types of currents: the current of electrons, the current of holes, and the displacement current caused by the electrostatic potential. Consequently, the total current density is given by

$$J_{tot} = J_n + J_p - \varepsilon \operatorname{grad} \partial_t V_t$$

This implies

$$\hat{j}_{S_k} = \int_{\Gamma_k} (J_n + J_p - \varepsilon \operatorname{grad} \partial_t V) \cdot \nu \, \mathrm{d}\sigma.$$
(16)

Note that the displacement current disappears if one considers the stationary case only. But it can not be neglected in the non-stationary case in order to guarantee charge conservation (see next sub-section).

The semiconductor current represents only one part of the coupling. The other part is given by the boundary condition (11) for the electrostatic potential V

$$V(x,t) = V_{\rm ap}(t) + V_{\rm bi}(x) \qquad \text{on } \Gamma_k.$$
(17)

 $V_{\rm ap}(t)$ is the applied potential at time t. It equals $e_j(t)$ if terminal k (corresponding to Γ_k) enters node number j. Regarding equation (14), we find that

$$V_{\rm ap}(t) = \begin{cases} 0 & \text{if terminal } k \text{ enters the mass node,} \\ \hat{a}_k^{\rm T} e(t) & \text{else} \end{cases}$$

for $\hat{a}_k^{\mathrm{T}} := (\hat{a}_{1k}, ..., \hat{a}_{n-1,k})$. Introducing the vector c(x) with the entries

$$c_k(x) = \begin{cases} 1 & \text{if } x \text{ belongs to } \Gamma_k, \\ 0 & \text{else,} \end{cases}$$

we may write equation (17) as

$$V(x,t) = c(x) \cdot \hat{A}_S^{\mathrm{T}} e(t) + W(x) \quad \text{on } \Gamma_k.$$
(18)

4.1 Charge Conservation and Homogenization

Before we turn to the investigation of the coupled system as an abstract differential algebraic system, we want to reformulate the system as follows. First, we use the model property of charge conservation which allows the replacement of \hat{A}_S by an incidence matrix A_S with the same properties as A_C , etc. Secondly, we homogenize the boundary conditions for the potential V in order to obtain function spaces that do not depend on the nodal potentials of the network.

The presented device model is charge conserving. In fact, we have

$$\operatorname{div}\left(J_n + J_p\right) = q(\partial_t n - \partial_t p)$$

if we add the continuity equations (7) and (8). Additionally,

div
$$(\varepsilon \operatorname{grad} \frac{\partial}{\partial t} V) = q(\partial_t n - \partial_t p),$$

if we differentiate the Poisson equation (6) with respect to time. This implies

$$\operatorname{div} J_{tot} = \operatorname{div} \left(J_n + J_p - \varepsilon \operatorname{grad} \partial_t V \right) = 0$$

and, by Gauss law,

$$\frac{\mathrm{d}Q}{\mathrm{d}t} = \oint_{\Gamma} J_{tot} \cdot \nu \, \mathrm{d}\sigma = \int_{\Omega} \mathrm{div} \, J_{tot} \, \mathrm{d}x = 0.$$

where Ω denotes the whole domain of the semiconductor. Furthermore, we get

$$\sum_{k} \hat{j}_{S_{k}} = \sum_{\substack{k \\ \Gamma_{k} \subseteq \Gamma_{O} \cup \Gamma_{S}}} \int_{\Gamma_{k}} (J_{n} + J_{p} - \varepsilon \operatorname{grad} \partial_{t} V) \cdot \nu \, \mathrm{d}\sigma$$
$$- \varepsilon \sum_{\substack{k \\ \Gamma_{k} \subseteq \Gamma_{MI}}} \int_{\Gamma_{k}} \operatorname{grad} \partial_{t} V \cdot \nu \, \mathrm{d}\sigma = \oint_{\Gamma} J_{tot} \cdot \nu \, \mathrm{d}\sigma = 0,$$

That means, that the current flowing through one terminal of the semiconductor may be described by the negative sum of the currents flowing through the other terminals. We choose one terminal (usual the bulk terminal) and call it the reference terminal. We delete the current of the reference terminal from \hat{j}_S and denote the resulting vector by j_S . This implies

$$\hat{A}_S \hat{j}_S = A_S j_S$$

if the entries of the matrix A_S are defined as

 $a_{ik} := \begin{cases} 1 & \text{if the current } j_{S_k} \text{ enters node } i, \\ -1 & \text{if the reference terminal is connected to node } i, \\ 0 & \text{else.} \end{cases}$

This way A_S has the same form as the other incidence matrices A_C , etc. Including the row for the mass node, each column of A_S has exactly one 1 and one -1. Finally, we arrive at

$$A_C C A_C^{\mathrm{T}} \frac{\mathrm{d}}{\mathrm{d}t} e + A_R G A_R^{\mathrm{T}} e + A_L j_L + A_V j_V + A_S j_S + A_I i_s = 0$$
(19)

instead of equation (15). Obviously, equation (16) is equivalent to

$$j_{S_k} = \int_{\Gamma_k} (J_n + J_p - \varepsilon \operatorname{grad} \partial_t V) \cdot \nu \, \mathrm{d}\sigma \tag{20}$$

Note that, in case of several semiconductors, one has to choose one reference terminal for each semiconductor and to follow the procedure for each semiconductor.

Considering existence results and Galerkin approaches for partial differential equations, Dirichlet boundary conditions are usually treated by a suitable choice of a function space as solution space. In our case, the Dirichlet boundary conditions (18) for the Poisson equation depend on the node potentials that are described only implicitly by the network equations. But it seems not comfortable to use a function space that is given implicitly only. Therefore, we shall homogenize the conditions (18) as follows.

From now we restrict to the one-dimensional case. Then, the semiconductor has only two boundary contacts. Without loss of generality, we choose x = 0 as the left and x = l as the right boundary, where l is the length of the semiconductor. Let f be a smooth function such that

$$f(0) = 1, \quad f(1) = 0, \quad f'(0) = 0.$$
 (21)

If the reference terminal enters node e_j , then we have

$$c \cdot \hat{A}_S^{\mathrm{T}} e = e_j + f(x) A_S^{\mathrm{T}} e$$
 on Γ

Introducing

$$\tilde{V}(x,t) := V(x,t) - e_j(t) - f(x) \cdot A_S^{\rm T} e(t) - V_{\rm bi}(x),$$
(22)

we get homogenous boundary conditions for \tilde{V} . Introducing

$$R(n, p, J_n, J_V, \operatorname{grad} V) := R(n, p, J_n, J_V, \operatorname{grad} V)$$

and summarizing all equations we arrive at the coupled system

$$A_C \frac{\mathrm{d}q(A_C^{\mathrm{T}}e)}{\mathrm{d}t} + A_R g(A_R^{\mathrm{T}}e) + A_L j_L + A_V j_V + A_S j_S = -A_I i_s, \quad (23)$$

$$\frac{\mathrm{d}\phi(j_L)}{\mathrm{d}t} - A_L^{\mathrm{T}}e = 0, \qquad (24)$$

$$A_V^{\mathrm{T}}e = v_s, \qquad (25)$$

$$\operatorname{div}\left(\varepsilon \operatorname{grad} \tilde{V}\right) = q(n-p-N) - \operatorname{div}\left(\varepsilon \operatorname{grad}\left(fA_{S}^{\mathrm{T}}e+V_{\mathrm{bi}}\right)\right), \quad (26)$$

$$\partial_t n - \frac{1}{q} \operatorname{div} J_n = -\tilde{R}(n, p, J_n, J_p, \operatorname{grad} \tilde{V}),$$
(27)

$$\partial_t p + \frac{1}{q} \operatorname{div} J_p = -\tilde{R}(n, p, J_n, J_p, \operatorname{grad} \tilde{V}), \qquad (28)$$

$$J_n = q(D_n \operatorname{grad} n - \mu_n n \operatorname{grad} (\tilde{V} + f A_S^{\mathrm{T}} e + V_{\mathrm{bi}})), \qquad (29)$$

$$J_p = q(-D_p \operatorname{grad} p - \mu_p \operatorname{grad} (V + f A_S^{\mathrm{T}} e + V_{\mathrm{bi}})), \qquad (30)$$

$$j_S = [(J_n + J_p) - \varepsilon \operatorname{grad} (\partial_t \tilde{V})]_{x=0}$$
(31)

with the boundary conditions

$$\tilde{V} = 0, \quad n = \frac{1}{2}(\sqrt{N^2 + 4n_i^2} + N), \quad p = \frac{1}{2}(\sqrt{N^2 + 4n_i^2} - N).$$
 (32)

5 The ADAS Index of the Coupled System

In order to treat coupled systems of partial differential equations and differentialalgebraic equations in a systematic way, abstract differential algebraic systems (ADASs) of the form

$$\mathcal{A}(t)\frac{\mathrm{d}}{\mathrm{d}t}(\mathcal{D}(t)u(t)) + \mathcal{B}(t)u(t) = q(t) \qquad \text{for } t \in [t_0, T]$$
(33)

have been investigated in [13]. This equation is to be understood as an operator equation with operators $\mathcal{A}(t)$, $\mathcal{D}(t)$ and $\mathcal{B}(t)$ acting in real Hilbert spaces. More precisely, let X, Y, Z and \tilde{Z} be Hilbert spaces and

$$\mathcal{A}(t) : Z \to Y, \quad \mathcal{D}(t) : X \to Z, \quad \mathcal{B}(t) : X \to Y.$$

As we will see later, we may formulate the coupled problem (23)-(32) as abstract DAEs with operators \mathcal{A} , \mathcal{D} and \mathcal{B} that do not explicitly depend on the time t. Therefore, we omit the argument t for these operators in the following. Having the coupled problem in mind, we assume \mathcal{A} to be linear.

From the finite dimensional case we know that the sensitivity of solutions of DAEs with respect to perturbations depends on its index. Since we are interested in the solution behavior of the coupled system, we want to follow the concept in [13] extending the tractability index to abstract DAEs. Therefore, we assume that the Fréchet derivatives \mathcal{B}_0 and \mathcal{D}_0 of the operators \mathcal{B} and \mathcal{D} exist. Furthermore, we introduce $\mathcal{G}_0 := \mathcal{AD}_0$.

Then, the abstract differential algebraic system

$$\mathcal{A}\frac{\mathrm{d}}{\mathrm{d}t}(\mathcal{D}u(t)) + \mathcal{B}u(t) = q(t) \qquad \text{for } t \in [t_0, T]$$
(34)

is called to have the index μ if

- (i) dim (im \mathcal{W}_i) > 0 for all $i = 0, ..., \mu 1$, dim (im \mathcal{W}_{μ}) = 0 and
- (ii) \mathcal{G}_{μ} is injective,

where \mathcal{W}_i and \mathcal{G}_i are recursively defined as follows. \mathcal{W}_i is a (bounded) projector with

$$\ker \mathcal{W}_i = \overline{\operatorname{im} \mathcal{G}_i}$$

Furthermore,

$$\mathcal{G}_{i+1} = \mathcal{G}_i + \mathcal{B}_i \mathcal{Q}_i, \qquad \mathcal{B}_{i+1} = \mathcal{B}_i \mathcal{P}_i,$$

where $\mathcal{P}_i = \mathcal{I} - \mathcal{Q}_i$ and \mathcal{Q}_i is a (bounded) projector with im $\mathcal{Q}_i = \overline{\ker \mathcal{G}_i}$.

We want to note here, that the so defined index for an abstract differential algebraic system shall characterize the behavior of the system with respect to time-dependent perturbations. It should not be confused with the Fredholm index of operators.

Omitting the tilde notation for the potential V of the coupled system (23)-(31) and regarding the boundary conditions (32), we are interested in solutions

$$u(t) = (e(t), j_L(t), j_V(t), j_S(t), V(\cdot, t), n(\cdot, t), p(\cdot, t), J_n(\cdot, t), J_p(\cdot, t))$$

that belong to

$$X := \mathbb{R}^{n_N - 1} \times \mathbb{R}^{n_L} \times \mathbb{R}^{n_V} \times \mathbb{R} \times (H^2(\Omega) \cap H^1_0(\Omega)) \times H^1(\Omega) \times H^1(\Omega$$

for the coupled system (23)-(31). Here, n_N , n_L and n_V denote the number of nodes, the number of inductances and the number of voltage sources of the network, respectively. Recall that $\Omega = (0, l)$. We need the higher regularity for V since we have to evaluate the gradient of V at the boundary for the determination of the semiconductor current j_S . Furthermore, we choose

$$Y := \mathbb{R}^{n_N - 1} \times \mathbb{R}^{n_L} \times \mathbb{R}^{n_V} \times (L_2(\Omega))^5 \times \mathbb{R}$$
$$Z := \mathbb{R}^{n_C} \times \mathbb{R}^{n_L} \times \mathbb{R}^{n_V} \times \mathbb{R} \times (H^1(\Omega))^2,$$

where n_C denotes the number of capacities in the network. Note that the we have to choose $(L_2(\Omega))^{5n_S}$ and $(H^1(\Omega))^{2n_S}$ instead of $(L_2(\Omega))^5$ and $(H^1(\Omega))^2$, respectively, if the network contains not only one but n_S semiconductors. Correspondingly, the space X has also to be extended.

We may write the coupled system (23)-(31) as ADAS

$$\mathcal{A}\frac{\mathrm{d}}{\mathrm{d}t}(\mathcal{D}u(t)) + \mathcal{B}u(t) = q(t) \qquad \text{for } t \in [t_0, T]$$
(35)

with

and

$$\mathcal{B}(u) = \begin{pmatrix} A_R g(A_R^* e) + A_L j_L + A_V j_V + A_S j_S \\ -A_L^T e \\ A_V^T e \\ \operatorname{div}\left(\varepsilon \operatorname{grad} \tilde{V}\right) - q(n-p-N) + \operatorname{div}\left(\varepsilon \operatorname{grad}\left(fA_S^T e + V_{\operatorname{bi}}\right)\right) \\ -\frac{1}{q} \operatorname{div} J_n + \tilde{R}(n, p, J_n, J_p, \operatorname{grad} V) \\ \frac{1}{q} \operatorname{div} J_p + \tilde{R}(n, p, J_n, J_p, \operatorname{grad} V) \\ J_n - q(D_n \operatorname{grad} n - \mu_n \operatorname{ngrad}\left(V + fA_S^T e + V_{\operatorname{bi}}\right)\right) \\ J_p + q(-D_p \operatorname{grad} p - \mu_p \operatorname{pgrad}\left(V + fA_S^T e + V_{\operatorname{bi}}\right)) \\ j_S - \mathfrak{r} J_n - \mathfrak{r} J_p \end{pmatrix}$$

where \mathfrak{r} is the restriction operator onto the left boundary, i.e.

$$\mathfrak{r}J(\cdot,t) := J(0,t).$$

From [23] we know that the network DAEs have an index ≤ 1 if the network neither contains *LI*-cutsets nor CV-loops with at least one voltage source. LIcutsets are cutsets including inductances and current sources only. CV-loops are loops containing capacitances and voltage sources only. As we will see in the next theorem, LI-cutsets are also relevant for the coupled systems. However CV-loops have to be replaced by CVS-loops, i.e. loops consisting of capacitances, voltage sources and semiconductors.

Remark 5.1 The network does not contain LI-cutsets if and only if the matrix

$$(A_C, A_R, A_V, A_S)$$
 has full row rank. (36)

The network does not contain CVS-loops with at least one voltage source or one semiconductor if and only if the matrix

$$(Q_C^T A_V, Q_C^T A_S)$$
 has full column rank (37)

for any projector Q_C onto ker A_C^{T} .

Furthermore, we will need the capacitance, resistance and inductance matrices

$$C(v) := \frac{\mathrm{d}q(v)}{\mathrm{d}v}, \quad G(v) := \frac{\mathrm{d}g(v)}{\mathrm{d}v}, \quad and \quad L(j) := \frac{\mathrm{d}\phi(j)}{\mathrm{d}j},$$

respectively.

and

Theorem 5.2 Let C(v), G(v) and L(j) be positive definite for all voltages v and currents j. Then, the ADAS (35) has index 1 if and only if the network contains neither LI-cutsets nor CVS-loops with at least one voltage source or one semiconductor.

Proof: We start with the Fréchet derivatives

For brevity, we have introduced

$$\begin{aligned} \mathfrak{j}_n(u) &:= q\mu_n \mathrm{grad} \left(V + f A_{\mathrm{S}}^{\mathrm{T}} e + V_{\mathrm{bi}} \right) - q D_n \mathrm{grad} \,, \\ \mathfrak{j}_p(u) &:= q\mu_p \mathrm{grad} \left(V + f A_{\mathrm{S}}^{\mathrm{T}} e + V_{\mathrm{bi}} \right) + q D_p \mathrm{grad} \,. \end{aligned}$$

Then, the image space of

is given by

$$\operatorname{im} A_C^{\mathrm{T}} \times \mathbb{R}^{n_L} \times 0 \times 0 \times H^1(\Omega) \times H^1(\Omega) \times 0 \times 0 \times \mathbb{R}.$$

Obviously,



is a projection operator along $\overline{\operatorname{im}} \mathcal{G}_0(u)$ for any projector Q_C onto the nullspace of A_C^{T} . Furthermore, $\mathcal{G}_0(u)$ has the nontrivial nullspace

$$\ker A_C^{\mathrm{T}} \times 0 \times \mathbb{R}^{n_V} \times \mathbb{R}^{n_S} \times \mathcal{N}_{\mathfrak{r}\,\mathrm{grad}} \times 0 \times 0 \times H^1(\Omega) \times H^1(\Omega)$$

where

$$\mathcal{N}_{\mathfrak{r}\,\mathrm{grad}} = \{ v \in H^2(\Omega) \cap H^1_0(\Omega) : \ \mathfrak{r}\,\mathrm{grad}\, v = 0 \}$$

We choose

$$\mathcal{Q}_{\mathfrak{r}\,\mathrm{grad}} : H^2(\Omega) \cap H^1_0(\Omega) \longrightarrow H^2(\Omega) \cap H^1_0(\Omega)$$
$$u \mapsto v$$

with

$$v(x) = u(x) - \frac{x(l-x)}{l} \mathfrak{r} \operatorname{grad} u \qquad \forall x \in \Omega$$

as projection operator onto $\mathcal{N}_{\mathfrak{r}\,\mathrm{grad}}\,.$ Then,

is a projection operator onto $\ker \mathcal{G}_0.$ It yields

(ii) We show that $\mathcal{G}_1(u)$ is not injective if the network contains LI-cutsets or CVS-loops.

If the network contains an LI-cutset, then we find a nontrivial $w \in \mathbb{R}^{n_N-1}$ with

$$A_C^{\mathrm{T}}w = 0, \ A_R^{\mathrm{T}}w = 0, \ A_V^{\mathrm{T}}w = 0, \ A_S^{\mathrm{T}}w = 0.$$

It implies $w = Q_C w$ and, consequently,

$$v_{\cdot} = (w, L^{-1}(j_L)A_L^{\mathrm{T}}w, 0, 0, 0, 0, 0, 0, 0)$$

belongs to the nullspace of $G_1(u)$.

If the network contains a CVS-loop, then we find a nontrivial $(w_1, w_2, w_3) \in \mathbb{R}^{n_C} \times \mathbb{R}^{n_V} \times \mathbb{R}^{n_S}$ with

$$A_C w_1 + A_V w_2 + A_S w_3 = 0$$

Since C(u) is positive definite, we find a $w_4 \in \operatorname{im} P_C$ such that

$$A_C w_1 = A_C C (A_C^{\mathrm{T}} e) A_C^{\mathrm{T}} w_4.$$

Choosing $w_5 \in H^2(\Omega) \cap H^1_0(\Omega)$ with

$$w_5(x) = \frac{x(l-x)}{\varepsilon \, l} w_3$$

the vector function

$$v_{\cdot} = (w_4, 0, w_2, w_3, w_5, 0, 0, 0, 0)$$

belongs to the nullspace of $G_1(u)$. The existence of at least one voltage source or one semiconductor in the CVS-loop ensures that the constructed v is nontrivial because at least w_2 or w_3 is nontrivial.

(ii) Now we show that $\mathcal{G}_1(u)$ is injective if the network contains neither LIcutsets nor CVS-loops with at least one voltage source or one semiconductor. We assume that

$$\mathcal{G}_1(u)w = 0. \tag{38}$$

Integration of the 4th line

$$\varepsilon \Delta f A_S^{\mathrm{T}} Q_C w_1 + \varepsilon \Delta Q_{\mathfrak{r} \operatorname{grad}} w_5 = 0$$

yields to

$$\operatorname{grad} f A_S^{\mathrm{T}} Q_C w_1 + \operatorname{grad} Q_{\mathfrak{r} \operatorname{grad}} w_5 = \mathfrak{r} \operatorname{grad} f A_S^{\mathrm{T}} Q_C w_1 = 0,$$

since \mathfrak{r} grad f = 0 for the chosen function f (see (21)). Integrating once again, we obtain

$$fA_S^{\mathrm{T}}Q_Cw_1 + Q_{\mathfrak{r}\,\mathrm{grad}}\,w_5 = \mathfrak{r}\,fA_S^{\mathrm{T}}Q_Cw_1 + \mathfrak{r}\,Q_{\mathfrak{r}\,\mathrm{grad}}\,w_5 = A_S^{\mathrm{T}}Q_Cw_1,\quad(39)$$

because $\mathfrak{r} f = 1$ (cf. (21)) and $Q_{\mathfrak{r} \text{grad}} w_5$ belongs to $H_0^1(\Omega)$. Evaluating this equation at the point x = l, we arrive at

$$0 = A_S^{\mathrm{T}} Q_C w_1.$$

From the 3rd line of (38), we know that $A_V^T Q_C w_1 = 0$. The 1st line of (38) implies

$$Q_{C}^{\mathrm{T}}A_{R}G(u)A_{R}^{\mathrm{T}}Q_{C}w_{1} + Q_{C}^{\mathrm{T}}A_{V}w_{3} + Q_{C}^{\mathrm{T}}A_{S}w_{4} = 0.$$

Regarding the two relations for w_1 above and the fact that G(u) is positive definite, we obtain

$$(A_C A_R A_V A_S)^{\mathrm{T}} Q_C w_1 = 0, \quad \text{i.e.} \quad Q_C w_1 = 0,$$
 (40)

since the network does not contain LI-cutsets. Furthermore, using the 2nd and the 5th-8th line of (38) as well as the fact that L(u) is positive definite, we get

$$w_2 = 0$$
, $w_8 = w_9 = 0$, $w_6 = w_7 = 0$.

The 1st line of (38) reads as

$$A_C C(u) A_C^{\rm T} w_1 + A_V w_3 + A_S w_4 = 0$$

now and implies

$$A_C^{\mathrm{T}} w_1 = 0, \quad w_3 = 0, \quad w_4 = 0,$$

since C(u) is positive definite and the network does not contain CVS-loops with at least one voltage source or one semiconductor. Together with (40) and the 9th line of (38), this yields to

$$w_1 = 0$$
, \mathfrak{r} grad $w_5 = 0$.

Regarding (39) and (40), we find

$$Q_{\mathfrak{r}\,\mathrm{grad}}\,w_5 = 0\tag{41}$$

and, consequently, $w_5 = 0$. That means that $G_1(u)$ is injective.

(iii) It remains to show that $\overline{\operatorname{im}} \mathcal{G}_1(u) = Y$ if the network contains neither LI-cutsets nor CVS-loops with at least one voltage source or one semiconductor. Since $C^{\infty}(\Omega)$ is dense in $L_2(\Omega)$, it is enough to show that

$$\mathbb{R}^{n-1} \times \mathbb{R}^{n_L} \times \mathbb{R}^{n_V} \times (C^{\infty}(\Omega))^5 \times \mathbb{R} \subseteq \operatorname{im} \mathcal{G}_1(u).$$

Let y belong to $\mathbb{R}^{n-1} \times \mathbb{R}^{n_L} \times \mathbb{R}^{n_V} \times (C^{\infty}(\Omega))^5 \times \mathbb{R}$. With similar arguments like above, one can show that the matrix

$$\begin{pmatrix} A_C C(u) A_C^{\mathrm{T}} + A_R G(u) A_R^{\mathrm{T}} Q_C & A_V & A_S \\ A_V^{\mathrm{T}} Q_C & 0 & 0 \\ A_S^{\mathrm{T}} Q_C & 0 & 0 \end{pmatrix}$$

is nonsingular because the network contains neither LI-cutsets nor CVSloops with at least one voltage source or one semiconductor. Consequently, we find w_1 , w_3 and w_4 such that

$$A_{C}C(u)A_{C}^{\mathrm{T}}w_{1} + A_{R}G(u)A_{R}^{\mathrm{T}}Q_{C}w_{1} + A_{V}w_{3} + A_{S}w_{4} = y_{1},$$

$$A_{V}^{\mathrm{T}}Q_{C}w_{1} = y_{3},$$

$$A_{S}^{\mathrm{T}}Q_{C}w_{1} = -\frac{1}{\varepsilon}\int_{0}^{l}\int_{0}^{\tau}y_{4}(s)\,\mathrm{d}s\,\mathrm{d}\tau.$$
(42)

Defining

$$\begin{split} w_2 &= L^{-1}(u)y_2 + L^{-1}(u)A_L^{\mathrm{T}}Q_C w_1, \\ w_5(x) &= \frac{1}{\varepsilon} \int_0^x \int_0^\tau y_4(s) \, \mathrm{d}s \, \mathrm{d}\tau + (1 - f(x))A_S^{\mathrm{T}}Q_C w_1 \\ &+ \frac{x(l-x)}{\varepsilon l}(w_4 - y_9 - \mathfrak{r} \, y_7 - \mathfrak{r} \, y_8), \\ w_8 &= y_7 - \frac{q}{\varepsilon} \mu_n n \int_0^x y_4(s) \, \mathrm{d}s, \\ w_9 &= y_8 - \frac{q}{\varepsilon} \mu_p p \int_0^x y_4(s) \, \mathrm{d}s, \\ w_6 &= y_5 - \tilde{R}_V(u) \mathrm{grad} \, Q_{\mathfrak{r} \, \mathrm{grad}} \, w_5 - \tilde{R}_{J_n}(u) w_8 + \frac{1}{q} \mathrm{div} \, w_8 - \tilde{R}_{J_p}(u) w_9 \\ w_7 &= y_6 - \tilde{R}_V(u) \mathrm{grad} \, Q_{\mathfrak{r} \, \mathrm{grad}} \, w_5 - \tilde{R}_{J_n}(u) w_8 - \tilde{R}_{J_p}(u) w_9 - \frac{1}{q} \mathrm{div} \, w_9 \end{split}$$

successively, we obtain $w \in X$ and $\mathcal{G}_1(u)w = y$. Note that (42) ensures that $w_5(l) = 0$ in order to guarantee $w_5 \in H_0^1(\Omega)$.

Conclusions

The further miniaturization demands a refined network analysis describing certain semiconductor elements by contributed models. Using the instationary drift-diffusion model, the device equations represent a system of elliptic and parabolic differential equations. The network is described by a differentialalgebraic system. Both systems are mutually coupled via boundary conditions.

The coupled system can be analyzed as abstract differential algebraic system. For the one-dimensional case (with respect to position), network topological criteria are described that guarantee index-1 systems. It is still an open question for the higher-dimensional case. Furthermore, it will be of interest under which conditions the coupled system has index 2.

The presented index results form a basis for the discussion of suitable discretizations of the coupled system. Following the method of lines one should look for space discretizations that imply a DAE with the same index as the abstract DAE has.

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